This listing of the claims will replace all prior versions, and listings of claims in the application.

In the Claims

1. (Currently amended) A system that provides a user of a single analog line multiple uses of said line comprising;

[[A]] a modem connected to a fixed logic system which multiplexes or demultiplexes data;

said modem compressing a signal traveling through said analog line;

said modem providing simultaneous transmission of two, or more, speech or data calls.

2. (Original) The system of claim 1 wherein said modem is programmable.

REMARKS

Specification

The abstract of the disclosure is objected to because the abstract is too short. Correction is required. See MPEP § 608.01(b). Applicant has amended the abstract accordingly.

Arrangement of the specification

The Examiner has objected to the arrangement of the specification. Accordingly, applicant has amended the specification and encloses a marked-up copy and clean copy herewith.

Claim Rejections – 35 USC § 102

Claims 1-2 are rejected under 35 U.S.C. 102(b) as being anticipated by Jacobi et al U.S. Patent No 6,249,531 B1.

As per claim 1, Jacobi et al teaches a system that provides user of a single line multiple uses of said line comprising: a modem (see fig. 3 element 14), said modem compressing a signal traveling through said analog line (see fig. 3 element 28 and abstract and col. 2, lines 40-58 and col. 3, lines 8-20, 55-67); said modem providing simultaneous transmission of two or more speech or data calls see col. 4, lines 1-5 and col. 5, lines 15-27).

Applicant has amended the claim to include that the modem is connected to a logic system which multiplexes or demultiplexes the data. Jacobi does not teach this. Therefore, Claim 1 is not anticipated or obvious over the prior art.

As per claim 2, Jacobi et al teaches wherein said modem is computer is known in the art as a (programmable) device (see col. 2, lines 5-6).

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For the reasons stated above for Claim 1, Claim 2 is not anticipated or obvious over the prior art.

Applicant now believes that the application is in condition for allowance.

Respectfully submitted,

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SOFT CHIP MODEM

RELATED APPLICATIONS

The present invention is a continuation of provisional patent application number 60/405,508.

BACKGROUND OF THE INVENTION

Handel-C was developed at Oxford University's Computing Laboratory and will allow electronic devices to be instantly programmed and indefinitely reprogrammed to perform any number of different functions. Its novel "software compiler" system operates in conjunction with a new generation of FPGA, or Field Programmable Gate Array< chips. A microprocessor has a fixed number of functions that cannot be altered after manufacture. FPGA chips posses an almost limitless range of functions and flexibility.

SUMMARY OF THE INVENTION

The SoftChip modem incorporates Handel-C, an ultra-fast programming language, designed around a simple timing model that makes it very accessible to system architects and software engineers. The SoftChip modem functions as a line splitter, can be reprogrammed as needed verses re-engineered, provides new functionality via software downloads, on analog lines as well as cable, satellite and fiber lines. This is not a temporary interruption, to provide service, but simultaneous use of the line.

BRIEF DESCRIPTION OF DRAWINGS

FIG. 1; a flow chart illustrating and embodiment of the present invention.

FIG. 2; a block diagram illustrating the present invention.

DETAILED DESCRIPTION OF INVENTION

The attached flow chart and block design describes the Soft Chip modem aimed to provide the simultaneous transmission of two, or more, speech or data calls and to provide flexible deployment of value added services implemented on plain old telephone service (POTS) lines.

To provide simultaneous transmission of two or more speech or data calls conventional analog voice traffic is compressed to occupy less bandwidth, thus freeing up the remaining bandwidth for additional voice or data traffic. A speech compression algorithm that provides sufficient quality of sound and requires only between 5.6 and 6.4 kbps of bandwidth is embedded within the SoftChip system.

To provide flexible deployment of value added services implemented on phone connections, the SoftChip modem incorporates Field Programmable Gate Array (FPGA) chips, hosting downloadable logic.

The SoftChip system is connected to a conventional copper line by an internal COTS modem. This would support two speech calls plus protocol overhead and be implemented as a plug-in board with a common interface that can be used for more powerful modems including digital ISDN and ADSL.

In one embodiment, a pair of SoftChip modems is used, one at each end of the analog link; the first compresses and multiplexes the data at the source end of the line,

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the second de-multiplexes and expands the data at the exchange end of the copper link.

Figure 1 shows the SoftChip modem of the present invention. Figure 1 shows a modem COTS V. 3428.8 KBPS (upgradeable to V 90 56 KBPS or higher as a plug in device.

The modem goes to a fixed logic system which multiplexes or demultiplexes data. It can add protocol layers as TCP/IP and H.323 to use as into an IP phone. The fixed logic system then can flow into a variable logic system. The functionality can be downloaded via modem. The functionality is organized as sequential components in a data processing stream. Component algorithms are as follows: encode/decode based PGP; DRM management with credentials accessed via modem and logic downloading protocol. Storing data is as follows: system identification; system state (version and description of variable logic as ordered list of components); PGP public/private keys and digital rights management keys. The system provides fixed logic content downloading. This information can be transferred to phone ports or data ports.

Figure 2 shows two scenarios of the present invention. In scenario modem one SoftChip modem is providing information to a second SoftChip modem. No additional information is required on the central switch side. Each SoftChip modem may have more than one voice/data source attached to it.

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Scenario two comprises one SoftChip modem providing information to two different destinations. In this scenario additional multiplexing environment is required on the central switch side.

CLAIMS

1. A system that provides a user of a single analog line multiple uses of said line comprising; A modem; said modem compressing a signal traveling through said analog line; said modem providing simultaneous transmission of two, or more, speech or data calls.

2. The system of claim 1 wherein said modem is programmable.

<u>ABSTRACT</u>

A system and method that provides a user multiple, simultaneous uses of a single analog line. <u>The system comprises a modem, wherein the modem compresses a signal traveling through the analog line. The modem provides simultaneous transmission of two or more speech or data calls.</u>

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